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Shaolun RUAN

Yong WANG

Singapore Management University, yongwang@smu.edu.sg

Weiwen JIANG

Ying MAO

Qiang GUAN

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RUAN, Shaolun; WANG, Yong; JIANG, Weiwen; MAO, Ying; and GUAN, Qiang. VACSEN: A Visualization Approach for Noise Awareness in Quantum Computing. (2023). *IEEE Transactions on Visualization and Computer Graphics*. 29, (1), 462-472.

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VACSEN: A Visualization Approach for Noise Awareness in Quantum Computing

Shaolun Ruan, Yong Wang, Weiwen Jiang, Ying Mao, and Qiang Guan

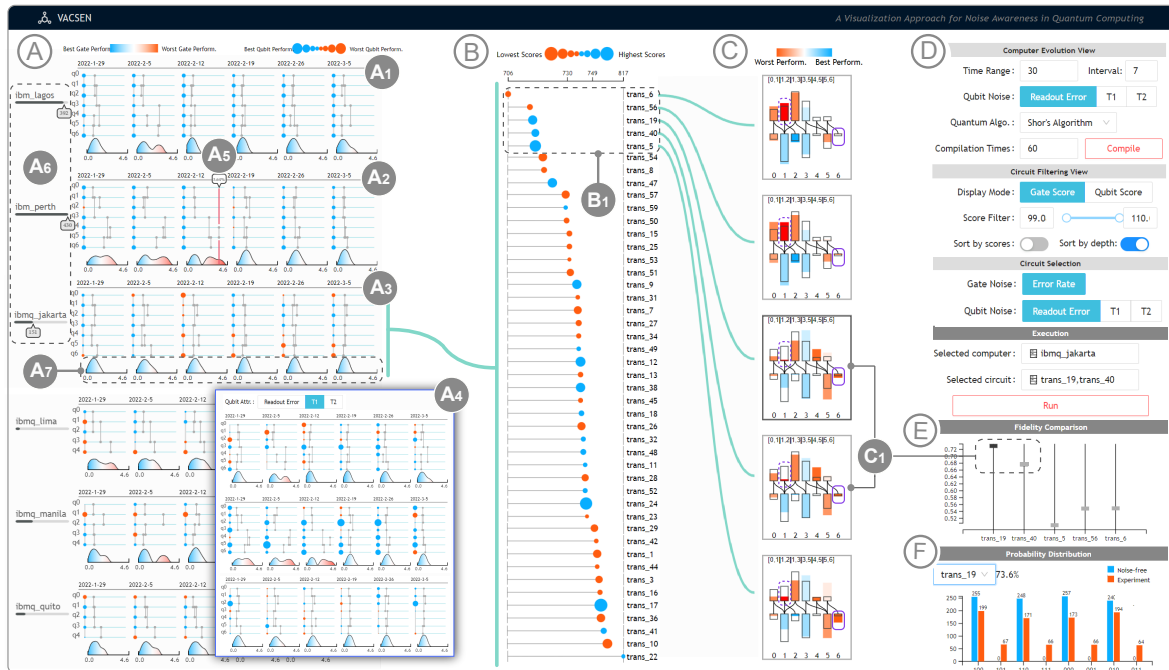


Fig. 1: The interface of VACSEN makes users aware of the quantum noise via three linked views (A-C). Computer Evolution View (A) allows the assessment for all quantum computers based on a temporal analysis for multiple performance metrics. Circuit Filtering View (B) supports the filtering for the potential optimal compiled circuits. Circuit Comparison View (C) supports the in-depth comparison regarding the performance of qubits or quantum gates and corresponding usages. The control panel (D) allows users to interactively configure the settings of VACSEN. Fidelity Comparison View (E) shows the fidelity distribution of each compiled circuit. Probability Distribution View (F) visualizes the results of state distribution of a quantum circuit execution.

Abstract—Quantum computing has attracted considerable public attention due to its exponential speedup over classical computing. Despite its advantages, today's quantum computers intrinsically suffer from noise and are error-prone. To guarantee the high fidelity of the execution result of a quantum algorithm, it is crucial to inform users of the noises of the used quantum computer and the compiled physical circuits. However, an intuitive and systematic way to make users aware of the quantum computing noise is still missing. In this paper, we fill the gap by proposing a novel visualization approach to achieve noise-aware quantum computing. It provides a holistic picture of the noise of quantum computing through multiple interactively coordinated views: a Computer Evolution View with a circuit-like design overviews the temporal evolution of the noises of different quantum computers, a Circuit Filtering View facilitates quick filtering of multiple compiled physical circuits for the same quantum algorithm, and a Circuit Comparison View with a coupled bar chart enables detailed comparison of the filtered compiled circuits. We extensively evaluate the performance of VACSEN through two case studies on quantum algorithms of different scales and an in-depth interviews with 12 quantum computing users. The results demonstrate the effectiveness and usability of VACSEN in achieving noise-aware quantum computing.

Index Terms—Data visualization, quantum computing, noise awareness

1 INTRODUCTION

- S. Ruan and Y. Wang are with Singapore Management University. Yong Wang is the corresponding author. E-mail: slruan.2021@phdcs.smu.edu.sg and yongwang@smu.edu.sg.
- W. Jiang is with George Mason University. E-mail: wjiang8@gmu.edu.
- Y. Mao is with Fordham University. E-mail: ymao41@fordham.edu.
- Q. Guan is with Kent State University. E-mail: qguan@kent.edu.

Manuscript received xx xxx. 201x; accepted xx xxx. 201x. Date of Publication xx xxx. 201x; date of current version xx xxx. 201x. For information on obtaining reprints of this article, please send e-mail to: reprints@ieee.org. Digital Object Identifier: xx.xxxx/TVCG.201x.xxxxxxx

Along with the evolution of actual quantum computers from industry, like IBM, IonQ, Rigetti, and D-Wave, quantum computing research has been rapidly growing in the past few years. An increasing number of quantum algorithms have been developed and shown a significant speedup over their best-known classical counterparts [5, 25, 32, 49]. On top of the breakthroughs in quantum algorithms, quantum computing has become a promising method in many important applications such as chemistry simulation [37] and machine learning [1, 11, 22, 58].

Despite the promising impacts on speeding up algorithms, the high

noise level in quantum devices is one of the key challenges to achieve the real quantum advantage in today’s quantum computers, which are well known as *Noisy Intermediate-Scale Quantum (NISQ)* computers. The noise originates from a quantum computer’s fundamental components [38], including (1) *qubits*, which are the basic units to store the quantum states; and (2) *quantum gates*, which are the basic operators to manipulate qubits’ states. For example, due to the volatility and limited coherence time, flip errors can occur at qubits [13].

The quantum noise issue becomes more severe due to the variation of the topology (*i.e.*, connection) on physical qubits. Specifically, quantum algorithms are synthesized as quantum circuits (also called *logical circuits* [48]). The quantum circuits will finally be deployed to the physical qubits for execution. It will go through the process of mapping logical circuits to physical qubits to obtain the physical quantum circuits (also called *compiled circuits* [51, 56]). The compiled circuit, however, is specified to the actual quantum computer since the topology of qubits is different. Thus, even for the same quantum algorithm, the effects of quantum noise on the algorithm can be significantly different.

In NISQ era, it is crucial to improve the fidelity of quantum computing, enabling the correctness of quantum algorithms (or at least approximate to the desired results). To this end, designers of quantum algorithms need to have a clear understanding of the hidden noise of different quantum computers as well as the noise inside different compiled circuits on the same quantum computer for a specific quantum algorithm [47]. However, this is not a trivial task. On the one hand, the access to most of today’s quantum computers has been provided by IT companies, like IBM, Amazon, and Microsoft, as a cloud service. To the best of our knowledge, no uniform tool revealing the noise exists to assist the designers. On the other hand, due to the rapid increase of quantum computing users [21], we face the increasing queuing time for accessing cloud quantum computers without noise awareness. Thus, the evaluation and mitigation of noise before execution has become even more urgent to avoid the waste of queuing time for unsatisfied results. Currently, the common practice to obtain less-noisy execution results is still a trial-and-error process.

To address the above problems, it is essential to have a tool to effectively inform users of the noise in quantum computing for a better selection of quantum computers and compiled circuits. Given that visualization has shown great power in various applications [34], we aim to develop a novel visualization approach to enhance users’ noise awareness in quantum computing and make quantum computing more transparent and reliable. However, there exist many challenges that mainly come from two perspectives: *complex and dynamically-evolving quality of quantum computers* and *significant variations of the compiled circuits*. First, the performance of quantum computers relies on multiple factors of qubits and quantum gates [23, 40, 42, 45], such as quantum decoherence, gate error, and qubit readout error. These factors are dynamically changing over time [7]. It is challenging to visualize these complex factors as well as the qubit topological connections along a timeline. Second, a quantum algorithm can be compiled to various compiled circuits with significantly-different noise on the same quantum computer. For a large-scale quantum algorithm, the compiled circuits can be several hundreds [8, 44]. The noise of compiled physical circuit needs to be evaluated from different perspectives, *e.g.*, the circuit depths and the noise of involved qubits or quantum gates [6]. But it is difficult to visually summarize a large number of the compiled circuits regarding the various noises, and enable users to select the most appropriate one shortly.

To fill the research gap, we propose *VACSEN*¹, a **V**isualization **A**pproa**C**h for noi**S**e awaren**E**ss in qua**N**tum computing. *VACSEN* can inform quantum computing users of the noise in quantum computers and compiled physical circuits, leading to a better execution result with higher fidelity. We follow a user-centered design process [33] by working closely with five domain experts in quantum computing for over five months. A pilot study is conducted to derive the design requirements. These design requirements guide our subsequent visual

¹*VACSEN* is pronounced as “vaccine”. We envision *VACSEN* can provide insights for designers to vaccinate their quantum algorithms against noise.

designs for *VACSEN*. *VACSEN* mainly consists of three novel visualization views: Computer Evolution View, Circuit Filtering View, and Circuit Comparison View. Specifically, Computer Evolution View (Fig. 1(A)) facilitates the temporal noise assessment of quantum computers by a novel circuit-like design that reveals the qubit connectivity. Circuit Filtering View (Fig. 1(B)) supports the filtering of the compiled circuits, allowing users to pick the compiled circuits of interest. Circuit Comparison View (Fig. 1(C)) further enables a more detailed comparison of selected compiled circuits with a novel coupled bar chart design, facilitating the selection of an optimal compiled circuit for the final execution. To the best of our knowledge, *VACSEN* is the first visualization approach for real-time noise awareness in quantum computing.

To evaluate the usefulness and effectiveness of *VACSEN*, we present two case studies on both small-scale and large-scale quantum algorithms and conduct in-depth interviews with 12 target quantum computing users. The results show that *VACSEN* can provide users with an intuitive way to be aware of quantum noise. The major contributions of this paper can be summarized as follows:

- We formulate the design requirements for interactive visual analysis of the noise in quantum computers and compiled circuits, together with quantum computing experts.
- We present an interactive visual analytics approach, *VACSEN*, to help quantum computing users assess the noise in quantum computing. Two novel visual designs are proposed: a circuit-like design facilitates the temporal analysis of quantum computer noise, and a coupled bar chart design enables the in-depth comparison of compiled circuits.
- We conduct two case studies and in-depth user interviews with expert users to demonstrate the effectiveness and usability of *VACSEN*.

2 RELATED WORK

Our work is relevant to prior research on visualization for quantum computing and the reliability improvement in quantum computing.

2.1 Visualization for Quantum Computing

Researchers have attempted to explain quantum computing via visualization. Miller *et al.* [31] proposed a node-link approach to present quantum circuits. Lin *et al.* [28] introduced a method to reveal the parameters’ dynamic change by the sequence of quantum gates. Tao *et al.* [53] introduced an interactive platform for users to better understand Shor’s algorithm. Also, there are online platforms that support an interactive implementation of circuits. For example, *Quirk* [39] provides a graphical tool to make users aware of quantum circuit’s behaviors and state changes. Some commercial vendors also provide cloud platforms that allow users to build quantum circuits interactively, *e.g.*, IBM Quantum² and Amazon Rigetti³.

All of the above studies focused on visualizing qubit states for a better understanding of the quantum workflow. The existing quantum computing platforms (*e.g.*, IBM Quantum) often provide users with some quality indicators of each qubit. However, these platforms can not make users aware of the noise in the compiled circuit, making the circuit execution less reliable. Our system aims to enable multi-level noise awareness in quantum computing through a novel visualization approach and then help enhance the fidelity of the execution of a quantum algorithm with noise awareness.

2.2 Reliability Improvement in Quantum Computing

Reliability improvement studies different methods to enhance the probability of correct results of quantum circuit execution, and has become a prominent research topic in quantum computing [29, 50, 54]. There are three major ways to enhance the reliability in quantum computing [6], *i.e.*, building better qubits, quantum error correction, and qubit

²IBM Quantum: <https://www.ibm.com/quantum-computing/>

³Rigetti: <https://www.rigetti.com/>

re-mapping. To build better qubits, the decoherence time of qubits has been improved significantly in the last two decades [4, 20]. However, qubits and gate operations can be made more robust and accurate from a fabrication and control perspective, and there are still some bottlenecks. Quantum error correction [27] requires significant qubit overhead (e.g., 18X extra qubits in the surface code [19]). Furthermore, quantum computing researchers have studied methods of optimizing the mapping from logical qubits to more reliable physical qubits by considering the noise of physical qubits [15, 36]. Ash-Saki *et al.* [6] provided an approach where high-quality qubits are prioritized for mapping. Bhattacharjee *et al.* [10] proposed a mapping approach considering the nearest neighbor compliance. Alam *et al.* [2] studied a qubit mapping algorithm to optimize circuits by exploiting gate re-ordering.

However, existing quantum computers still suffer from noises and the above approaches cannot achieve error-free quantum computing. Thus, it is still crucial to provide users with an intuitive way to understand and compare the noise of both quantum computers and compiled circuits, which is the focus of this paper.

3 BACKGROUND

This section introduces the overall background of quantum computing which is relevant to our study, including quantum computer architecture, quantum computing workflow, fidelity and noise in quantum computing.

3.1 Quantum Computer Architecture

Qubit, or quantum bit, is the basic unit of quantum information in quantum computing. For classical computing, a bit always has two deterministic states, i.e., “0” or “1”. For quantum computing, there are two orthonormal basis states for a qubit, i.e., $|0\rangle$ and $|1\rangle$. The actual quantum state of a qubit can be represented by a linear *superposition* of the two basis states [12]:

$$|\Psi\rangle = \alpha|0\rangle + \beta|1\rangle, \quad (1)$$

where α, β are complex numbers subject to $|\alpha|^2 + |\beta|^2 = 1$ [12]. Also, multiple qubits can be entangled to support the qubit interactions, which is called *quantum entanglement* [18]. An entangled state of the two qubits can be made via a gate on the control qubit, followed by the so-called CNOT gate [3].

Quantum Gate, or quantum logical gate, is a basic quantum circuit operating on qubits to modify the qubit states. For example, *Pauli* gate is for a single qubit, and *CNOT* (Control-NOT) gate is a commonly-used quantum operation applied on two qubits simultaneously. Like classical logic gates, where the conventional digital circuits are the basic building blocks, quantum gates are also the building blocks of quantum circuits.

3.2 Quantum Computing Workflow

The general *quantum workflow* includes quantum circuit implementation, quantum computer selection, and qubit mapping & execution [57].

Quantum Circuit Implementation. Quantum circuit implementation is the first step of the quantum workflow where users implement a quantum circuit to realize a quantum algorithm. A logical quantum circuit is ready to be compiled and executed by connecting multiple qubits with quantum gates. There are many ways to implement a quantum circuit. For example, the IBM Quantum platform supports circuit implementation by an online graphical interface, and the implementation by programming with *Qiskit*⁴ is also supported.

Quantum Computer Selection. After implementing a usable logical quantum circuit, users are required to select a preferred quantum computer for further execution. This step is totally up to users to decide which quantum computer is appropriate. A constraint for quantum computer selection is the required number of qubits for a given circuit. For example, the quantum computer *ibmq_armonk* in IBM Q platform with only one qubit is not appropriate to host a circuit with five qubits required. A quantum computer with a better performance of qubits and

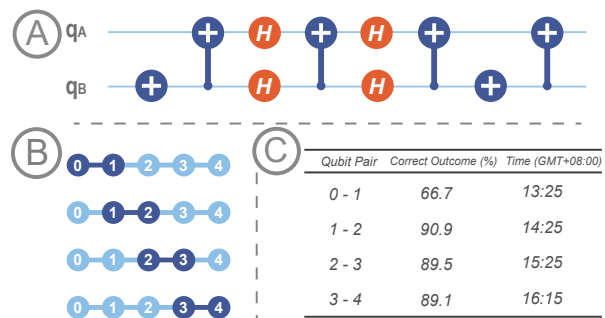


Fig. 2: An example of qubit mapping on quantum computer *ibmq_bogota*. (A) the test logical circuit. The symbol “+” indicates the CNOT gate, and “H” indicates the Hadamard gate which creates the superposition states. (B) Five options of qubit mapping on physical qubits of *ibmq_bogota*. No SWAP gate is needed due to the direct connection for all options. (C) Correct output probabilities for all mapping options. The experiment has been conducted on January-8-2022.

quantum gates would be more reliable to execute a quantum circuit [6]. In addition, the job queuing number determines the waiting time for a circuit execution. Thus, quantum computer selection is a complex task.

Qubit Mapping & Execution. As mentioned in Section 1, the logical circuit will be mapped to a “physical quantum circuit” [55] to fit the connectivity of the selected computer. The qubit mapping procedure depends on the mapping algorithms and cannot be modified by users. The mapped physical circuit can be different even for the same logical circuit [55]. Fig. 2(B) shows four different mapped physical circuit options for the same logical circuit (Fig. 2(A)). Since the mapping is a blind mapping without any noise awareness, the probability of the correct results would be different due to the performance variation of qubits and quantum gates [6] (Fig. 2(C)).

3.3 Fidelity & Noise in Quantum Computing

VACSEN uses the *fidelity* of the execution results to assess the *qubit error* and *gate error* in quantum computers and compiled circuits.

Fidelity. Fidelity is a metric for measuring the difference between ideal execution results (noise-free) and actual results. For today’s NISQ computer, the execution of a quantum algorithm is error-prone, leading to biased results of measurements compared with a noise-free result. The higher the fidelity is, the more accurate and valid an execution is.

Qubit Error. A qubit can retain the state for only a limited period of time, and this duration is called *decoherence time*. There are two metrics to measure the decoherence error, i.e., *relaxation time T1* and *dephasing time T2*. T1 affects the state $|1\rangle$ (i.e., $|1\rangle \rightarrow |0\rangle$), leaving state $|0\rangle$ invariant. However, it is also possible that qubit may interact with the environment and encounter a phase error, and the time constant associated with this decay is called dephasing time T2. Besides the decoherence error, errors can also exist in the readout of the qubit state, which is called the *readout error* [7, 52].

Gate Error. Gates are operations controlling multiple qubits. They can also affect qubit states due to the noise. Gate error rate is defined as the probability of introducing an error while performing operations [24]. Different types of gates may have different impacts on fidelity. For example, a recent study reveals the error rate of a 2-qubit gate (e.g., CNOT gate) can cause the maximum loss of fidelity [6].

4 INFORMING THE DESIGN

We conducted a pilot study to identify the general workflow and derive design requirements for noise awareness in quantum computing.

4.1 Pilot Study

We designed a pilot study following the methodology introduced by Sedlmair *et al.* [46]. Specifically, we invited five quantum computing experts (P1-5) to participate in the pilot study. P1 is a research scientist

⁴Qiskit: <https://qiskit.org/>

from Pacific Northwest National Laboratory, **P2-4** are professors or post-doc researchers from three different universities, and **P5** is a Ph.D. student majoring in quantum computing. Among them, **P1** and **P2** are doing research on quantum chemistry. **P3** is working on quantum machine learning, and is one of the co-authors of this work. **P4** and **P5** study the temporal patterns of quantum hardware’s quality. All the domain experts have an average of 4.9 years of research and development experience in quantum computing.

We began the pilot study by performing one-on-one, semi-structured, hour-long interviews with all participants. We categorized all participants into two groups. First, Group 1 (**P1-3**) were asked several questions about their understandings and solutions to noise awareness in quantum computing. Based on their feedback, we formulated the initial design requirements and proposed a low-fidelity demo system. Next, we presented the demo system to Group 2 (**P4-5**), who can provide in-depth suggestions for the noise analysis based on their calibration data analysis experiences of quantum computers. We discussed the demo system with Group 2 iteratively. They were asked several questions about their suggestions and concerns; we tuned the demo system accordingly. We observed and took notes during the meetings to ensure that our early system meets the basic requirements for the quantum computing scenario. Second, we presented the early system and held weekly meetings with Group 2 over the next two months. They were asked to explore the prototype system freely and complete the analysis tasks for noise-aware quantum circuit execution. We recorded each meeting and then held open discussions separately. The suggestions collected in this round were used to polish our system further.

4.2 Design Requirements

According to the feedback from all the experts, there are two major steps for executing a quantum algorithm on a quantum computer: **quantum computer selection** and **compiled circuit selection**. Even on the same quantum computing platform, different quantum computers can have significantly different hardware properties such as gate-count, qubit-count, performance data, and qubit connectivity. Also, selecting a better compiled circuit can improve the fidelity and reliability of execution due to the noise variation of the compiled circuits. We summarized six design requirements for the evaluation and mitigation of noise.

For the quantum computer selection, users need to select the optimal quantum computer to host the quantum algorithm, which requires users to be aware of quantum noise from the following aspects:

- R1 Facilitate the temporal analysis of qubit and gate noise.** All participants (**P1-5**) confirmed that it is crucial to enable the temporal exploration of the qubit and quantum gate noise. Specifically, the temporal analysis of quantum computers’ status will be more accurate than the reflection under a single timestamp. **P1** also commented that it will be intuitive to reveal the periodical patterns of qubits’ performance [7] via a visualization approach.
- R2 Make users aware of the latest quantum computing noise.** **P1** and **P5** confirmed that besides tracing the historical noise of quantum computers, it is also important to be informed of the latest noise status, since the time-varying quantum hardware properties can change quickly when the calibration launches. **P4** also reported that the real-time reflection of the queuing job number will also be helpful for quantum computer selection.

For the compiled circuit selection, the following requirements are crucial for informing the quantum noise users of different noise:

- R3 Provide an overview of all compiled circuits.** According to the suggestions from four participants (**P2-5**), more compilation results can increase the likelihood of selecting an optimal compiled circuit. Thus, it will be helpful to allow users to select multiple compiled circuits of their interest for an in-depth comparison.
- R4 Enable a detailed comparison of the usages of implemented qubits and gates.** All participants (**P1-5**) emphasized the need for a drilling-down comparison of selected compiled circuits. In addition, **P4** also pointed out that it is necessary to provide the reference value of the performance for a better noise comparison.

R5 Support a real-time compilation and fidelity validation of quantum algorithms. All participants (**P1-5**) confirmed that the system should support real-time compilation of quantum algorithms on real quantum computers to generate the latest compiled circuits. Also, the preferred compiled circuit needs to be further executed on the selected quantum computer, which can help users validate the performance of the selected quantum computer and compiled circuits for running the selected algorithm.

For the overall visual designs, experts also requested flexible interactions and intuitive visualizations:

R6 Provide flexible user interactions and intuitive visual designs.

Three participants (**P1, P3-4**) commented that the system needs to support on-demand analysis for quantum computing noise assessment. For example, some casual users will focus on the quantum gate error rate to handle simple quantum circuits, while other users may be interested in the qubit decoherence time when executing a complex quantum circuit like a quantum machine learning model. Flexible user interactions can support such requirements. Also, concise design is preferred, as quantum computing users often do not have a background in data visualization.

4.3 Dataset

Guided by the above design requirements, we collected the following data from the cloud quantum platform (*i.e.*, IBM Quantum), which indicates quantum noise and will be visualized in *VACSEN*:

- **Calibration data of quantum computers.** We collected the latest relaxation time T_1 , dephasing time T_2 , qubit readout error, and gate error rate. We also collected the queuing number of the quantum computer from the remote quantum cloud computing platform.
- **Properties of compiled physical circuits.** Such properties need to be extracted from real-time compilation, which includes the usage of each qubit and quantum gate, and the execution results of a quantum algorithm from the cloud quantum computing platform. Following the prior studies [16, 41, 59], we further calculate the fidelity of each compiled physical circuit by using the Hellinger distance [30].

5 VACSEN

We propose *VACSEN*, an interactive visualization approach to inform users of the noise in quantum computing when trying to execute their quantum algorithms on quantum computers. *VACSEN* can be accessed via the URL: <https://vacsen.github.io/>.

Fig. 3 shows the architecture of *VACSEN*. *VACSEN* is designed to work with quantum computing cloud platforms (*e.g.*, IBM Quantum), and inform users of the quantum noise in the quantum circuit compilation and execution. *VACSEN* consists of three modules: storage module, processing module and visualization module. The storage module stores all the available latest calibration data, including qubit properties and gate properties. The processing module handles the quantum algorithm’s implementation and the compiled circuit’s extraction from the cloud quantum computing platform. The visualization module reveals the noise of quantum computers and compiled circuits.

After selecting the optimal compiled circuit, *VACSEN* also supports a post-execution analysis for a quantum algorithm. Fidelity Comparison View (Fig. 1(E)) supports the validation of the compiled circuits’ fidelity. The vertical coordinate of the dot indicates the fidelity value. The dot highlighted in black in Fidelity Comparison View denotes the fidelity value of the compiled circuit selected by the user. Also, users can further check their detailed state distributions by clicking the dot. The corresponding execution result will be visualized on Probability Distribution View (Fig. 1(F)), where the x-axis indicates the state distribution and the y-axis represents the corresponding shot numbers.

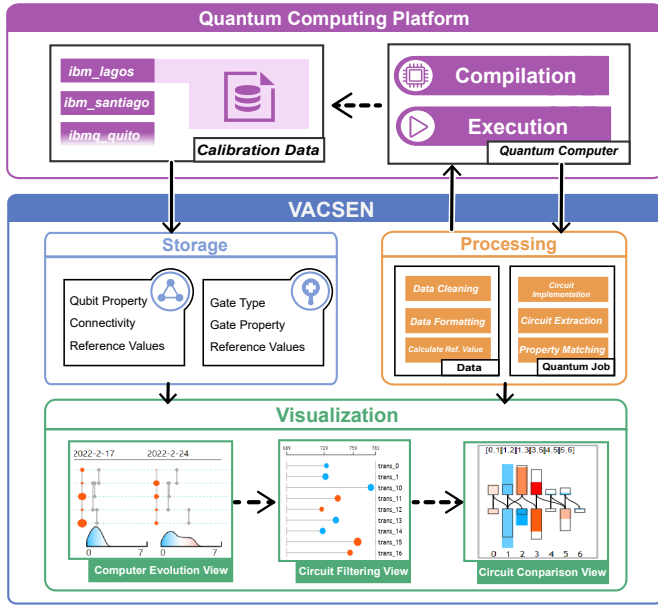


Fig. 3: The system architecture of VACSEN contains three modules (a storage module, a processing module, and a visualization module) and the remote quantum computing platform.

5.1 Computer Evolution View

We propose a circuit-like design to visualize the quantum computer noise in each timestamp. Following the prior studies [6, 7, 10], we encode qubit T1 time, qubit T2 time, qubit readout error rate, and quantum gate error rate to portray the noise in quantum computers. We utilize a sequence of circuit-like designs to visualize the time-varying performance of a quantum computer. Also, as shown in Fig. 1(A), the latest queuing number of the quantum computer is encoded by the horizontal bar chart.

Circuit-like Design. As shown in Fig. 4(A), the colored circles represent the value of one of the qubit noise attributes (*i.e.*, T1 time, T2 time, and readout error), which can be switched by the user. According to the feedback of domain experts, the relative noise across different qubits and gates is even more effective than their absolute noise values for quantum computers and compiled circuit selection. Thus, we first calculate the average of the noise attributes among all qubits as the reference value and further compute the difference between a noise attribute and the reference value. Specifically, the circle radius represents the absolute value of the difference between the readout error and the reference value. A circle will be colored in blue if the readout error is lower than the reference value. Otherwise, it will be colored in red. The larger the radius of the blue circle, the better the performance of the qubit; conversely, the larger the radius of the red circle, the worse the performance of the qubit; if the radius of the circle is close to 0, it means that the performance of the qubit is close to the average performance. Meanwhile, the quantum gate topology is encoded by the line segments in grey. The horizontal coordinate of each line segment indicates its gate error rate, while the vertical position of two endpoints denotes the two qubits that the quantum gate operates on. We adjust the opacity of line segments to mitigate the possible visual clutter issues due to overlapped line segments.

During the iterative weekly meetings with domain experts, they mentioned that it is difficult to understand the overall noise level of quantum gates. To this end, we visually summarized the overall distribution for quantum gates' noise using the Kernel Density Estimation (KDE) method. Given all quantum gates' error rate distribution list (x_1, x_2, \dots, x_n) , the KDE can be calculated as follows:

$$\hat{f}_h(x) = \frac{1}{nh} \sum_{i=1}^n K\left(\frac{x-x_i}{h}\right), \quad (2)$$

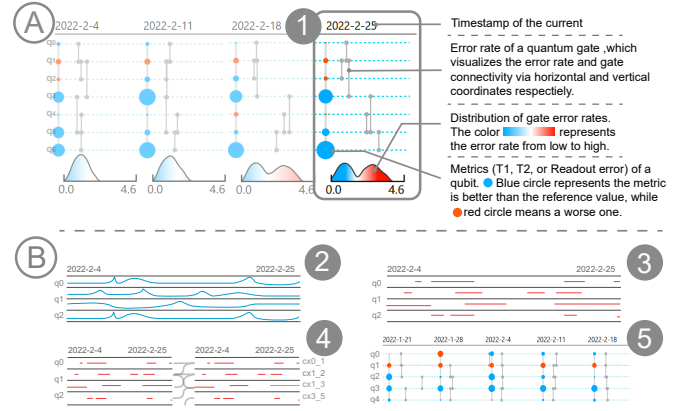


Fig. 4: Computer Evolution View. (A) The time-varying quality of all usable quantum computers are visualized using the uniform timeslicing approach. (B) Design alternatives of the Computer Evolution View. The alternative designs (2) and (3) cannot visualize the connections among qubits. While the other alternative designs either suffer from visual clutters (4) in visualizing qubit connections or cannot clearly indicate gate errors (5).

where K is the kernel - a non-negative function - and h is a smoothing bandwidth. We utilize the Gaussian function [17] as the kernel of the KDE distribution. As shown in Fig. 4(A), we visualize the KDE distribution at the bottom of each circuit-like design. Furthermore, we fill the density area chart with a gradient color (*i.e.*, from blue to red) to intuitively show the quantum gate noise distribution.

Design Alternatives. Before finalizing the current visual design, we also considered four design alternatives for the temporal analysis of the quantum computer noise (Fig. 4(B)). Fig. 4(2) is the initial design that is a general design for temporal analysis. However, it is not appropriate to visualize a number of qubits' noise trends simultaneously in a narrow space, making it difficult to observe the temporal patterns. To address the problem, we abstracted the anomalous periods and highlighted them with red line segments (Fig. 4(3)). During our weekly meeting with domain experts, all the experts (P1-5) commented that it would be better to embed the quantum gates' noise and their connectivity into the design. The design in Fig. 4(4) addressed this problem, but the curve lines denoting the gate connectivity will introduce a severe visual clutter when the qubit number is over five. Fig. 4(5) addressed the challenge by using a timeslice approach. However, as mentioned above, the quantum gate noise distribution is not intuitive for this design.

5.2 Circuit Filtering View

The Circuit Filtering View aims to help users filter several compiled circuits of their interest. To portray the overall performance of qubits and gates, we define a metric *overall performance scores*, including *qubit scores* and *gate scores*, for each compiled circuit. We then calculate the average of all scores as the reference value. As shown in Fig. 1(B), each row denotes a compiled circuit. The horizontal coordinate of the circle depicts the *depth* of the circuit, which indicates the number of instructions for a compiled circuit. The circle will be colored blue if the overall performance scores are higher than the reference value. Otherwise, it would be rendered in red. The user can configure in the control panel and indicate either gate scores or qubit scores to be visualized by the circles. The circle radius indicates the absolute value of the difference between the overall performance scores and the reference value. The larger the radius of the blue circle, the less noisy the circuit; conversely, the larger the radius of the red circle, the noisier the circuit; if the circle radius is closer to 0, it means that the circuit noise is closer to the average level.

For a compiled circuit, a physical quantum gate or qubit is often be used for multiple times. After discussing with the domain experts, we followed the idea of Linearly Weighted Moving Average and proposed

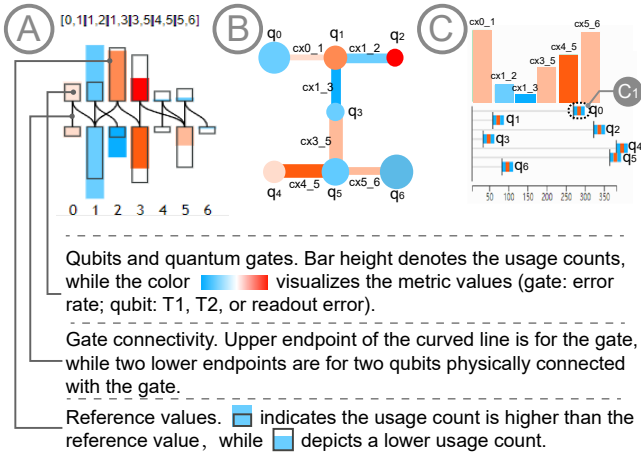


Fig. 5: Circuit Comparison View. (A) The Circuit Comparison View supports the in-depth comparison of multiple compiled circuits. (B)(C) Design alternatives of Circuit Comparison View. Both alternative designs cannot support an effective comparison of the usage times of qubits and quantum gates.

the overall score (S), which represents the weighted error rate across different qubits or gates to indicate the overall error rate of a compiled circuit. We choose the reciprocal of the overall error rate to represent its overall performance. Thus, given a compiled circuit with N quantum gates (or qubits), we define the overall score S as follows:

$$S = \left(\frac{\sum_{i=1}^N C_i \cdot E_i}{\sum_{i=1}^N C_i} \right)^{-1}, \quad (3)$$

where E_i denotes the error rate of a quantum gate (or qubit), and C_i is the usage times of a quantum gate (or qubit).

5.3 Circuit Comparison View

Circuit Comparison View supports the in-depth comparison of multiple compiled circuits. We propose a novel coupled bar chart to enable users to drill down to the previously selected compiled circuits. The design summarizes the instructions of the compiled circuits. It portrays the noise based on the performance metrics (*i.e.*, qubit T1 time, qubit T2 time, qubit readout error, and quantum gate error rate) and the usage times of the hosted qubits and quantum gates.

Coupled Bar Charts. As shown in Fig. 5(A), each coupled bar chart indicates the noise for a compiled circuit. The upper group of bars denotes the usage times of quantum gates of a compiled circuit, while the bottom group of bars represents the usage times of the qubits. The color of the upper bars represents the error rate of the gates, while the color of the lower bars denotes one of the three noise attributes for qubits (*i.e.*, T1, T2, and readout error). The darker the blue of the bar, the less the noise of the qubit or gate; the darker the red of the bar, the more noise of the qubit or gate. The height of the black rectangle indicates the average usage times of the gate or qubit among all compiled circuits. Meanwhile, the curved lines between the upper and lower groups of bars visualize the connectivity of quantum gates. Specifically, the upper endpoint of the curved line represents a quantum gate, while the corresponding two lower endpoints denote the two qubits operated on the quantum gate.

Design Alternatives. We considered other two design alternatives for the detailed comparison of compiled circuits regarding the noise. Fig. 5(B) shows the compiled circuit’s noise by the original topology of the selected quantum computers. The usage times of qubits and quantum gates are encoded by the circle radius and line segment width, respectively. The noise level is indicated by the same color encoding as the coupled bar chart. However, it is difficult to compare the line segment width between multiple compiled circuits. The second design

alternative is shown in Fig. 5(C), which encodes the gate usage times by the bar height and qubit usage times by the horizontal position of the glyph (Fig. 5(C₁)). However, we found that the comparison between multiple compiled circuits from horizontal and vertical directions simultaneously is confusing for the users. To address all these challenges, we further propose the coupled bar chart (Fig. 5(A)).

5.4 User Interactions

VACSEN enables rich interactions to help users smoothly explore and analyze the noise of different quantum computers and compiled circuits.

Hierarchical Noise Analysis. The Computer Evolution View (Fig. 1(A)) provides an overview of quantum computer noise. By clicking a quantum computer, users can view the noise of all the compiled circuits in the Circuit Filtering View (Fig. 1(B)), which can be further explored and compared in detail in the Circuit Comparison View (Fig. 1(C)).

Time Configurations. Users can adjust the time range of the noise to be shown in the Computer Evolution View by specifying the latest “*time range*” value in the control panel (Fig. 1(D)) and the time “*interval*” (Fig. 1(D)) can also be adjusted to show quantum computer noise with different granularity.

Quantum Algorithm Specifications. Users can specify the quantum algorithm to be run and the number of compilations in the control panel (Fig. 1(D)). Once the quantum algorithm is determined, users can launch the compilation of the quantum algorithm on the cloud platform by simply clicking the button “*Compile*” (Fig. 1(D)).

Interactive Filtering and Sorting of Compiled Circuits. Users can switch between “*Gate*” and “*Qubit*” to visualize the compiled circuits according to either gate scores or qubit scores. Also, *VACSEN* allows users to sort all compiled circuits in the Circuit Filtering View by the quality scores or circuit depth via the switch “*Sort by scores*” and “*Sort by depth*” in the control panel (Fig. 1(D)). Furthermore, users can filter circuits by their quality scores via the “*Score Filter*” range slider.

Remote Execution Controlling. After selecting the preferred compiled circuit for execution, the user can launch the execution via the button “*Run*” in the control panel (Fig. 1(D)). The remote cloud quantum computer would host and execute the selected compiled circuit.

6 CASE STUDY

In this section, we conducted two case studies on both small-scale and large-scale quantum circuits to demonstrate the effectiveness of *VACSEN*. The users involved in the case studies are two quantum computing experts (U1 and U6) who also attended the user interviews in Section 7. IBM Quantum platform was used to compile and execute the quantum circuits on March 5, 2022⁵.

6.1 Case Study I - Two-qubit Circuit

U1 employed *VACSEN* to explore the noise of different quantum computers and select an appropriate compiled circuit for the two-qubit circuit introduced by Ash-Saki *et al.* [6]. This circuit is often used for demonstrating the different probabilities of correct results.

Striking a trade-off between quantum computer noise and queuing time. Both quantum computer noise assessment and queuing number awareness are crucial for quantum computer selection, which can make the execution more reliable and time-saving. Given that the quantum circuit needs two qubits, eight potential quantum computers can be used, except *ibmq_armonk* with only one qubit. U1 first compared the noises of different quantum computers in the past week (R1) and set the time interval as one day in the control panel. U1 then set the “*Qubit Noise*” as the readout error (R6). By simply glancing at all the available quantum computers, U1 immediately noticed that *ibmq_perth* has the least qubit noise (*i.e.*, the best qubit performance) from 2022-2-27 to 2022-3-5, as indicated by the consistently large blue circles (Fig.

⁵Only nine out of 10 quantum computers are in service on March 5, 2022 (the date of our case studies), while the unavailable quantum computer *ibmq_santiago* is under maintenance

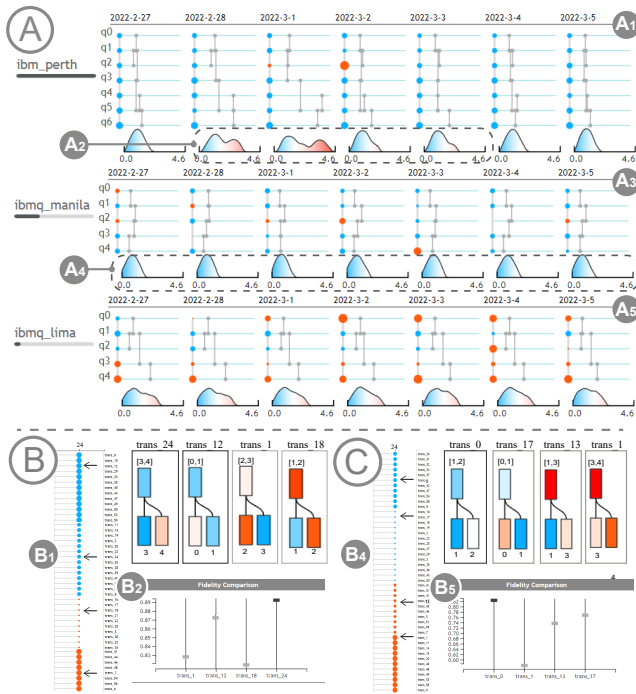


Fig. 6: The case for two-qubit circuit. (A) Computer Evolution View for temporal analysis of *ibmq_perth*, *ibmq_manila*, *ibmq_lima*. (B) Comparison of compiled circuits for *ibmq_manila*. Comparison of compiled circuits for *ibmq_lima*.

6(A₁). The gate error rate of *ibmq_perth* was rather low on the current day (i.e., 2022-3-5), though it fluctuated from 2022-2-28 to 2022-3-3 as indicated by the density area charts (Fig. 6(A₂)). He then noticed the quantum computer *ibmq_manila*, whose gate error rates have been stable in the past week, staying below 1% (Fig. 6(A₄)). Meanwhile, the qubit noise of *ibmq_manila* is quite low via inspecting the blue circles. Thus, U1 concluded that the noise of quantum computers *ibmq_perth* and *ibmq_manila* is the least among all quantum computers. U1 evaluated *ibmq_perth* and *ibmq_manila* to the same noise level regarding the qubit and gate errors. Furthermore, U1 noticed that the queuing numbers for *ibmq_perth* (i.e., 430 in total) were much larger than other computers, as indicated by the bar charts below the computer names. Given that the waiting time of *ibmq_perth* is a bit long, U1 finally selected *ibmq_manila* for the subsequent quantum circuit execution.

Unveiling the mask of low-fidelity compiled circuits. After selecting the appropriate quantum computer (i.e., *ibmq_manila*), U1 then moved on to the next step (i.e., quantum circuit compilation). U1 further used *VACSEN* to filter the suitable compiled circuits on *ibmq_manila*. U1 first changed the number of compilation times to 60 in the control panel. After a few seconds of compilation, all the compiled circuits were displayed in the Circuit Filtering View. Then, U1 sorted all the compiled circuits according to the gate scores (Fig. 6(B₁)). U1 noticed that the 60 compiled circuits have the same depth of 24, as indicated by the circles' horizontal coordinates. Meanwhile, from the radii of all the circles, U1 speculated that the two-qubit logical circuit can be compiled into four different physical circuits based on the qubit mapping algorithm. U1 randomly selected one compiled circuit from each category and switched to the Circuit Comparison View for a detailed comparison. U1 immediately noticed that the four circuits are assigned on different physical quantum gates on *ibmq_manila*, as indicated by the four coupled bar charts in Fig. 6(B). For example, Circuit *trans_24* with the highest gate scores assigned the CNOT gate on Gate *cx3_4* and Qubits *q3* and *q4*, whose gate error is minimal as indicated by the blue bar on the top. Also, U1 found that the two bars in Circuit *trans_18* were in dark red, indicating that *trans_18* utilized Gate *cx1_2* and Qubit *q2* with the highest error rate for the quantum circuit implementation. Thus,

U1 decided to select Circuit *trans_24* for the final execution. To test the effectiveness of *VACSEN* and make sure Circuit *trans_24* was the circuit with the least noise, U1 also executed the other three compiled circuits. Fig. 6(B₂) shows the fidelity distribution for the four compiled circuits. It was apparent that the preferred compiled circuit *trans_24* represented by the black rectangle had the highest fidelity (89.4%). In contrast, the fidelity of the other three compiled circuits was 87.5%, 83% and 82.1%, respectively. U1 finally got the best execution results with the least noise with the help of *VACSEN*.

Selecting the best of a bad bunch. U1 planned to execute the two-qubit circuit on another quantum computer with small queuing numbers to see if it is possible to generate a high-fidelity result with little queuing time. As shown in the Computer Evolution View (Fig. 6(A₅)), the performance of *ibmq_lima*'s qubits was below average as indicated by the red circles, and several gates (Gate *cx1_3* and Gate *cx3_4*) also performed badly in the past week according to their line segments' horizontal coordinates. However, due to the small queuing number of the quantum computer *ibmq_lima*, as indicated by the bar chart below the computer name, U1 planned to execute on the noisy quantum computer *ibmq_lima* to get a reliable result in a short waiting time. After the circuit compilation and sorting of all the compiled circuits in the control panel, U1 found four types of compiled circuits from Circuit Filtering View (Fig. 6(B₃)). U1 found that Circuit *trans_0* had the least noise due to the color of bars for the implemented Gate *cx1_2* and Qubits *q1* and *q2*. Thus, U1 selected the Circuit *trans_0* for the final circuit execution. To convince himself, U1 then executes other three compiled circuits. The fidelity of the execution is shown in Fig. 6(B₅). Circuit *trans_0* showed a much better fidelity (83.1%) than the other circuits (77.5%, 74.4% and 58.5%).

6.2 Case Study II - Shor's Algorithm

U6 used *VACSEN* to assess the quantum noise when trying to execute a large-scale quantum circuit, i.e., *Shor's algorithm*. Shor's algorithm [49] is a famous and widely-used quantum algorithm for integer factorization. Meanwhile, it is usually used for the evaluation of noise optimization algorithms [14, 43]. The experiment was conducted on the IBM Quantum platform on March 5, 2022.

Balancing different noises of a quantum computer. Since the Shor's algorithm requires seven qubits, the potential quantum computers are those with no less than seven qubits, i.e., *ibmq_lagos*, *ibmq_perth* and *ibmq_jakarta*. U6 changed the *time range* and *interval* to 30 and 7 respectively in the control panel, to explore their status in the past month. U6 first set the qubit noise to "readout error" in the control panel. U6 quickly found that *ibmq_jakarta* (Fig. 1(A₃)) has the most stable gate noise from the density area charts (Fig. 1(A₇)), while *ibmq_perth*'s gate error rate was much higher than the other two computers due to the three red areas from 2022-1-19 to 2022-2-12. Particularly, the error rate of Gate *cx4_5* (3.64%) on 2022-2-12 was significantly higher than that of the other gates (Fig. 1(A₅)). Since the decoherence time matters for large-scale quantum algorithms [6]. Thus, U6 changed the qubit noise to "T1" to assess quantum computers regarding the decoherence time. As shown in Fig. 1(A₄), U6 noticed that Computer *ibmq_perth*, whose gates are most noisy among all the three computers as illustrated above, has the best performance for T1 time as indicated by the large blue circles. Meanwhile, Computer *ibmq_lagos*'s T1 time is the shortest among the three computers, though *ibmq_lagos* has the best performance in terms of gate error rate as indicated by the consistently blue circles in Fig. 1(A₁). Thus, U6 concluded that the three quantum computers have diverse noise patterns. Taking into account all the factors, i.e., the qubit noise, gate noise, and queuing number (Fig. 1(A₆)), U6 decided to select Computer *ibmq_jakarta* for the final execution.

Breaking the tie: "similar" compiled circuits can be different. After selecting *ibmq_jakarta* as the preferred quantum computer, U6 launched the compilation to map Shor's algorithm on this computer. As shown in Fig. 1(B), the depths of all the compiled circuits were from 706 to 817, which were larger compared with the previous two-qubit quantum circuit. Meanwhile, U6 found that every circle has a different radius, indicating that a new compiled circuit was generated at every time of the compilation. Since minimizing the depth of the

T1	Find the best-quality quantum computer regarding qubit's relaxation time $T1$.
T2	Find the best-quality quantum computer regarding qubits' dephasing time $T2$.
T3	Find the best-quality quantum computer regarding qubits' readout error.
T4	Find the best-quality quantum computer regarding gates' error rate.
T5	According to the tasks above, find the most suitable computer for the further execution.
T6	Find the circuits of interest regarding the quality of building blocks.
T7	Find the circuits of interest regarding the circuit depth.
T8	Compare and highlight the compiled circuits with good gate-quality for the final execution.
T9	Compare and highlight the compiled circuits with good qubit-quality for the final execution.

Table 1: All tasks are grouped by the analysis workflow, *i.e.*, quantum computer selection and compiled circuit selection.

circuit can minimize the decoherence noise for a large-scale circuit [26, 60], U6 regarded circuit depth as the major factor for the compiled circuit selection and sorted all the 60 compiled circuits by depth in an ascending order via the switch “Sort by depth” in the control panel (Fig. 1(D)). U6 then clicked the top five circuits with a minor depth (Fig. 1(B₁)) for the comparison. Then, the five corresponding coupled bar charts were shown in Fig. 1(C). For the gate error rates, U6 noticed that Gate $cx1_2$, which is highlighted by the purple dotted box in all the five circuits, has the highest error rate as indicated by the dark red bar. U6 then found that $trans_19$, $trans_40$, and $trans_5$ used Gate $cx1_2$ much less frequently than the other two compiled circuits (*i.e.*, $trans_6$ and $trans_56$) as indicated by the small height of the three bars. Meanwhile, the usage times of the high-performance Gate $cx3_5$ in the above three circuits were much larger than those in the other two circuits, as indicated by the light blue bar's height. U6 commented, “Because the extra usage numbers of the high-noise gate $cx1_2$ were transferred to the low-noise gate $cx3_5$, making it more reliable.” For the qubit noise comparison, U6 noticed that Qubit $q6$, highlighted by the purple box, has the highest noise among all qubits as indicated by the dark red bars. Also, it was apparent that Circuit $trans_5$ used Qubit $q6$ most often as indicated by the bar heights, which was far above the reference value as highlighted by the black box. Thus, U6 decided to execute Circuit $trans_19$ due to its smaller depth than $trans_40$, though they have a similar noise level. After waiting for a while, the system displayed the fidelity distribution (Fig. 1(E)), showing that the fidelity of Circuit $trans_19$ (73.6%) was the highest among the five circuits, as U6 expected. Meanwhile, U6 found that the result fidelities of $trans_19$ and $trans_40$ were at the first tier of less-noise circuits (Fig. 4(C₁)) due to the similar visual evidence of the corresponding coupled bar charts, while the average fidelity for the other three circuits was 58%.

7 USER INTERVIEWS

To further evaluate the effectiveness of VACSEN, we conducted in-depth user interviews with the target users working on quantum computing.

7.1 Study Design

Participants and Apparatus. We invited 12 participants (2 females) from six different educational institutions and a national research laboratory to join our in-depth user interviews. The participants are different from the experts participated in the pilot study. Specifically, U1-5 are postgraduate students working on quantum computing. U6 is a research staff from a research laboratory for quantum machine learning. U7-12 are professors with an average of over five years of research experience in the quantum computing-related research fields. Among them, U8-9's current research direction is the qubit mapping algorithm. To guarantee that the findings from the interviews are general for common users,

Q1	The system provides enough evidence to inform the noise in quantum computer.
Q2	The system enables the exploration and comparison of the noise in different compiled circuits.
Q3	The system can facilitate noise mitigation for the quantum circuit execution.
Q4	The overall visual design is easy to understand.
Q5	The visual designs of circuit-like design are helpful for assessing the noise in quantum computers
Q6	The design of the coupled bar charts for compiled circuit comparison is effective.
Q7	The user interaction of the visualization is smooth.
Q8	The interaction to support the communication between the remote quantum computing platform is robust
Q9	The visual analytics system is easy to use
Q10	The visual analytics system is easy to learn
Q11	I would like to use the visual analytics system to mitigate the noise in quantum computing in the future.
Q12	I will recommend the visual analytics system to my colleagues working on quantum computing.

Table 2: The questionnaire consists of four parts: the effectiveness for quantum noise awareness (Q1-3), the visual design (Q4-6), the user interactions (Q7-8) and the usability (Q9-12).

none of the participants has a background in visualization or HCI. The user interviews were conducted through the online zoom meetings due to the COVID-19 pandemic. In addition, the participants were asked to use a monitor with a resolution of 1920×1080 in advance.

Quantum Algorithms and Tasks. We provided three common quantum algorithms for quantum circuit implementation, *i.e.*, Quantum Fourier Transform (QFT) algorithm [35] and Bernstein-Vazirani (BV) algorithm [9] (both with the depth of up to 50) and Shor's algorithm (with the depth of over 700). We asked participants to select the quantum algorithm of interest. Also, we asked participants to fulfill nine carefully-designed tasks to assess VACSEN, as shown in Table 1.

Procedures. The user interview for each participant was conducted using the online VACSEN system. We recorded and took notes for each interview and their interaction processes. We first introduced the analysis workflow and the corresponding visual designs of VACSEN to the participant. After that, we showcased an example (*i.e.*, the two-qubit circuit) to better illustrate the usage of VACSEN. The above tutorial lasted for about 25 minutes. After that, the participants were asked to accomplish the pre-defined tasks of selecting an appropriate quantum computer and an optimal compiled circuit for execution. Upon the exploration, participants were encouraged to describe the reasons for the selection in a think-aloud manner. The aforementioned tasks lasted about 40 minutes. We also invited every participants to rate the VACSEN system based on a 7-point Likert scale from three aspects shown in Table 2. Finally, we further conducted a post-study interview with each participants, which lasted about 25 minutes.

7.2 Result

We summarized all participants' detailed feedback as follows:

The Effectiveness for Noise Awareness. Most participants ($rating_{mean} = 5.83, rating_{sd} = 1.03$) agreed that VACSEN can facilitate noise awareness and mitigation in quantum computing. Four participants (U1-3, U11) highly appreciate the temporal noise analysis for quantum computers. U11 commented, “It is beneficial that I can visually analyze the time-series pattern of various noises. For example, I find that the qubit readout error of *ibmq.jakarta*'s $q5$ and $q6$ is always worse than that of other qubits, which is really intuitive” Also, three participants (U5-6, U10) commented that the proposed workflow to support the quantum computer and compiled circuit selection is quite helpful. U10 said, “In my daily work, I need to wait for even hours for my quantum machine learning circuit execution. Now, VACSEN can save me a great number of time to get a similar high-fidelity result from other quantum computers with much less queuing time.” In addition,

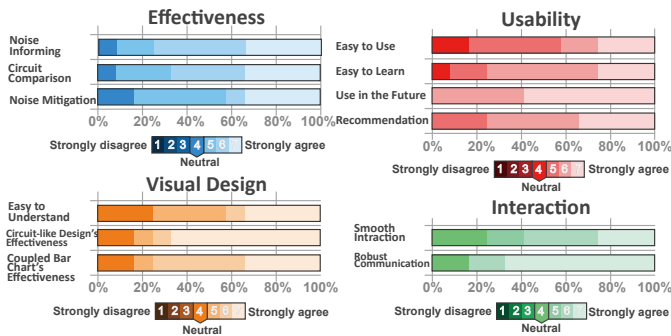


Fig. 7: The summary of the user feedback.

all participants (U1-12) were very interested in the communication between VACSEN and the remote quantum computing platform IBM Q. U8 reported that the real-time execution makes VACSEN even more valuable and practical compared with other theoretical approaches.

Usability. Most participants are in favor of the usability of VACSEN ($rating_{mean} = 5.88, rating_{sd} = 1.37$). U1-5 mentioned that VACSEN is friendly and easy to use for quantum computing researchers. Specifically, they confirmed that VACSEN is really easy to use. U4 commented that the interface is really what quantum computing users want to use due to the intuitive designs. U6 also praised VACSEN, “For quantum machine learning, the model training device’s noise state significantly impacts the model performance, and the prediction process also requires to be run on the quantum computer which has the same noise state as the one for model training. To this end, I believe the interface is the one we are looking for to complete the noise assessments”. Meanwhile, U8-9, who is doing research on qubit mapping algorithm, commented, “I believe VACSEN will be helpful for our current research topic of quantum network routing. We can utilize VACSEN to host our different routing algorithm as it can reflect various noises in real-time.” U5 mentioned he will utilize VACSEN for his daily circuit execution rather than a blind execution in the past. Meanwhile, he expressed the desire to recommend VACSEN to his colleagues in his research lab.

Visual Design and Interactions. The majority of the participants like the effective and user-friendly visual design ($rating_{mean} = 6.02, rating_{sd} = 0.81$) and flexible user interactions ($rating_{mean} = 6.04, rating_{sd} = 1.12$) of VACSEN. Four participants (U5-8) mentioned that the circuit-like design is amazing, which encodes the topology and metrics simultaneously for each timestamp. Meanwhile, U6 and U7 commented that the density area chart within each block of Computer Evolution View is very informative. U6 mentioned, “To my surprise, the design can visualize the distribution of all quantum gates’ error rates. I can glance at the gates’ density area charts and individual line segments and compare them between different quantum computers.” For the user interactions, U1-12 confirmed that the system’s interactions are really smooth and easy to use. U9 commented, “I found it is easy to switch between different noise attributes, which meets the needs to fit different quantum algorithm scenarios.”

Suggestions. Despite the positive feedback, several participants also gave suggestions to improve VACSEN. U2 suggested that synchronous circuit execution should be more time-saving. U11 pointed out the selection of quantum computers can consider the correlations between various noises. U10 commented that he would be happy if the VACSEN system could support his research domain - quantum machine learning.

8 DISCUSSION

In this section, we first summarize the lessons we learned during the development of VACSEN. Then, we discuss the limitations of VACSEN.

8.1 Lessons

We learned many lessons from the system design and implementation.

Critical importance of visualization for quantum computing. As shown in the above evaluations, VACSEN received highly positive feedback from the target users. Among all the feedback, they

emphasize the strong needs and critical importance of visualization approaches. As various noises originates from the inherent noise in current quantum computers, they really need an interactive way to investigate the reasons behind the errors of running a quantum algorithm. VACSEN is only the first step to address such kinds of needs in both visualization and quantum computing field, which can play an even more important role in reliable, accessible, and transparent quantum computing.

Intuitive visual designs matter much for quantum computing users. While designing the prototype of VACSEN, we attempted to visualize all the noise attributes of qubits and gates simultaneously, which resulted in a sophisticated visual design. However, during our regular meeting with the five domain experts (Section 4.1), they pointed out that it is really confusing for them to explore the system because learning the complex visual designs has a steep learning curve for target users. Therefore, we simplified the visual designs, and further proposed intuitive and novel designs, such as the circuit style design and coupled bar chart to ensure that each quantum computing user can easily use it.

8.2 Limitations

Our evaluations have shown that VACSEN can effectively enable noise awareness for the execution of quantum algorithms. However, the proposed approach still has limitations.

Calibration Data Resolution. VACSEN can extract the latest calibration data to portray the performance of a quantum computer. However, according to our analysis of the calibration data, we found that the actual resolution of the data is sometimes larger than one day. In addition, we found that the updating of the calibration data may suspend for a few days (e.g., *ibmq_bogota*).

Visual Scalability. According to the feedback of domain experts, general users will often access and use only quantum computers with a relatively small number of qubits (e.g., up to 16) in most situations. Our case studies and user interviews have confirmed that VACSEN can work well for these situations. However, VACSEN may suffer from scalability issues when it is used to explore quantum computers with an extreme number of qubits (e.g., 127 or even 400).

Generalizability. VACSEN is mainly tested using the data collected from IBM Quantum. However, it can be easily extended to other cloud quantum computing platforms (e.g., Rigetti), as the workflow and performance data of quantum computers are similar across different quantum computing platforms.

9 CONCLUSION

We present VACSEN, an interactive visual analytics approach to support noise awareness in quantum computing. To inform the visual design, we identified six design requirements for visually analyzing a quantum computing-specific problem. We proposed two novel quantum computing-specific designs, i.e., the circuit-like design and coupled bar chart, to facilitate the temporal analysis of noise in quantum computers and in-depth comparison of compiled circuits. We conducted case studies and in-depth user interviews with 12 target users to demonstrate the effectiveness of VACSEN. The results show that VACSEN can effectively make the users aware of various noises and further improve the reliability of the execution result of a given quantum circuit.

In future work, we will enhance the scalability of VACSEN in terms of facilitating noise evaluation on quantum computers with a larger number of qubits. Also, it will be interesting to enable automated recommendation of desirable quantum computers and compiled circuits in VACSEN and further enhance its usability and effectiveness in helping users select the optimal quantum computers and compiled circuit.

ACKNOWLEDGMENTS

This research was supported by the Lee Kong Chian Fellowship awarded to Yong Wang by Singapore Management University. Qiang Guan is supported by US National Science Foundation CCF-2217021 and IBM quantum hub at NC State. We would like to thank the participants in our user interviews and anonymous reviewers for their feedback.

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